A Designer's Guide to Asynchronous VLSI

Peter A. Beerel, Recep O. Ozdag, Marcos Ferretti

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Bypass the limitations of synchronous design and create low power, higher performance circuits with shorter design times using this practical guide to asynchronous design. The fundamentals of asynchronous design are covered, as is a large variety of design styles, while the emphasis throughout is on practical techniques and real-world applications.

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